

IN THE CLAIMS:

Please cancel claim 11 and amend claims 1-10, 12-14, 17, 23, 30-32, 34-35 and 37-43 as indicated in the following Listing of All Pending Claims of the present application.

Listing of All Pending Claims

1. (amended once) A wireless communications device, comprising:
- a wireless communications device circuit for communicating over an air interface;
 - indirectly-read memory that is not volatile;
 - data stored in the indirectly-read memory;
 - an addressable volatile memory coupled to the indirectly-read memory;
 - a first interface controller coupled to the indirectly-read memory and the addressable volatile memory;
 - interface logic coupled to the first interface controller wherein the interface logic and the first interface controller are configured to transfer the data from the indirectly-read memory to the addressable volatile memory; and
 - ~~a wireless communications device circuit;~~
 - a ~~second controller~~ processing unit coupled to the addressable volatile memory and the wireless communications device circuit, the processing unit ~~and~~ configured to control the wireless communications device circuit based upon the data stored in the addressable volatile ~~addressable~~ memory.
2. (amended once) The wireless communications device of claim 1, wherein the ~~indirectly-read~~ indirectly-read memory is clocked serial memory.
3. (amended once) The wireless communications device of claim 1, wherein the ~~indirectly-read~~ indirectly-read memory is clocked parallel memory.

4. (amended once) The wireless communications device of claim 1, wherein the ~~indirectly-read~~ indirectly-read memory is indexed addressable memory.

5. (amended once) The wireless communications device of claim 1, wherein the ~~indirectly-read~~ indirectly-read memory is addressable, serially interfaced memory.

6. (amended once) The wireless communications device of claim 1,
wherein the addressable volatile memory is dynamic random access memory or static random access memory; and

wherein the ~~serial~~ indirectly-read memory is chosen from the set of serial memory consisting of MultiMediaCard, SmartMedia Card, SD Card and Memory Stick.

7. (amended once) The wireless communications device of claim 1,
wherein the ~~first~~ interface controller ~~with logic~~ transfers only a portion of the data stored in the indirectly-read memory to the addressable volatile memory, and

wherein as the mobile ~~communications~~ communications device requires additional data stored in the ~~serial~~ indirectly-read memory ~~but not transferred to the volatile memory~~, the ~~first processor will cause~~ processing unit causes the memory interface controller ~~with memory interface logic~~ to transfer the required additional data from the ~~serial~~ indirectly-read memory to the addressable volatile memory.

8. (amended once) The wireless communications device of claim 1, further comprising:
a read only memory that contains boot code; and
volatile memory,

wherein when the wireless communications device is powered on, the boot code is executed and the ~~first~~ interface controller determines whether indirectly-read memory is connected to the wireless communications device, and

wherein if the ~~indirectly-read~~ indirectly-read memory is connected to the wireless communications device, the ~~first~~ interface controller ~~with logic~~ transfers and interface

logic transfer data stored in the ~~indirectly-read~~ indirectly-read memory to the volatile memory.

9. (amended once) The wireless communications device of claim 8 4, wherein the volatile memory is the wireless communications device circuit.

10. (amended once) The wireless communications device of claim 1, wherein the ~~indirectly-read~~ indirectly-read memory comprises NAND Flash.

11. (cancelled) ~~The wireless communications device of claim 1, wherein the first controller and the second controller are coupled to a processor.~~

12. (amended once) The wireless communications device of claim 1, wherein the ~~first controller and the second controller are coupled to~~ processing unit is a microprocessor.

13. (amended once) The wireless communications device of claim 1, wherein the ~~first controller and the second controller are coupled to~~ processing unit is a microcontroller.

14. (amended once) The wireless communications device of claim 1, wherein the ~~first interface controller and the second controller~~ processing unit are a single controller.

15. (original) The wireless communications device of claim 14, wherein the single controller is a processor.

16. (original) The wireless communications device of claim 14, wherein the single controller is a microprocessor.

17. (amended once) The wireless communications device of claim 14, wherein the single processor is a microcontroller.

18. (original) The wireless communications device of claim 1, wherein the data stored in the indirectly-read memory is data representing non-critical operations.

19. (original) The wireless communications device of claim 1, wherein the data stored in the indirectly-read memory is data representing critical operations.

20. (original) The wireless communications device of claim 19, wherein the data representing critical operations is an operating system.

21. (original) The wireless communications device of claim 19, wherein the data representing critical operations are calibration parameters.

22. (original) The wireless communications device of claim 19, wherein the data representing critical operations is an application program that is critical to the operation of the wireless device.

23. (amended once) The wireless communications device of claim 18, wherein the data representing non-critical ~~operation~~ operations is user interface information.


24. (original) The wireless communications device of claim 18, wherein the data representing non-critical operations is a recent call list.

25. (original) The wireless communications device of claim 18, wherein the data representing non-critical operations are display settings.

26. (original) The wireless communications device of claim 18, wherein the data representing non-critical operations are roaming preferences.

27. (original) The wireless communications device of claim 18, wherein the data representing non-critical operations are ringer preferences.

28. (original) The wireless communications device of claim 18, wherein the data representing non-critical operations is an application program that is not critical to the operation of the wireless device.

 29. (original) The wireless communications device of claim 18, wherein the data representing non-critical operations is a phone book.

30. (amended once) The wireless ~~communication~~ communications device of claim 1, wherein the wireless ~~communication~~ communications device circuit comprises a power amplifier.

31. (amended once) The wireless ~~communication~~ communications device of claim 1, wherein the wireless ~~communication~~ communications device circuit comprises a user interface.

32. (amended once) A method for managing a memory in a wireless ~~communication~~ communications device, comprising the steps of:

providing a serial interface controller for sending a start signal to a clocked, non-addressable, non volatile memory from a controller;

reading data from the clocked, non-addressable, non volatile memory;

transferring the data to a volatile[[,]] addressable memory;


reading the data from the volatile addressable memory;

controlling a wireless communication device circuit responsive to the data.

33. (original) The method of claim 32, wherein the volatile memory is dynamic random access memory or static random access memory.

34. (amended once) The method of claim 33 wherein the ~~serial~~ the clocked, non-addressable, non volatile memory is chosen from the set of serial memory consisting of MultiMediaCard, SmartMedia Card, SD Card and Memory Stick.

35. (amended once) The method of claim 32, wherein the wireless communications device is a portable radio telephone, the method further comprising the step of:

 determining whether the ~~serial~~ the clocked, non-addressable, non volatile memory is connected to the portable radio telephone, and wherein only if the ~~serial~~ the clocked, non-addressable, non volatile memory is connected to the portable radio telephone, then transferring data stored in the ~~serial~~ the clocked, non-addressable, non volatile memory to the volatile memory.

36. (original) The method of claim 35, wherein the volatile memory is dynamic random access memory or static random access memory.

37. (amended once) The method of claim 36 wherein the ~~serial~~ the clocked, non-addressable, non volatile memory is chosen from the set of serial memory consisting of MultiMediaCard, SmartMedia Card, SD Card, and Memory Stick.

38. (amended once) The method of claim 32, wherein only a portion of the data stored in the ~~serial~~ the clocked, non-addressable, non volatile memory is transferred to the volatile addressable memory, and wherein the method further comprises the steps of:
requesting additional data stored in the ~~serial~~ the clocked, non-addressable, non volatile memory ~~but not transferred to the volatile memory~~ and transferring the additional data to volatile memory.

39. (amended once) A wireless ~~communication~~ communications device system, comprising:

a portable radio telephone, comprising:

an addressable volatile memory;

a first serial interface controller coupled to the addressable volatile memory;

interface logic coupled to the first serial interface controller;

a wireless communications device circuit coupled to the first serial interface controller; and

a second controller coupled to the volatile memory and the wireless communications device circuit, and configured to control the wireless communications device circuit based upon the data stored in the volatile addressable memory; and

an accessory, comprising;

indirectly-read memory that is not volatile; and
data stored in the indirectly-read memory;

wherein the interface logic and the first serial interface controller are configured to transfer the data from the indirectly-read memory to the addressable volatile memory.

40. (amended once) The wireless ~~communication~~ communications device system of claim 39, wherein the first serial interface controller the interface logic separates the an address and from the data read from the serial indirectly-read memory to produce separated data, and converts the separated data from serial data to parallel data, as wherein the parallel data is transferred to the volatile memory.

41. (amended once) The wireless ~~communication~~ communications device system of claim 39,

wherein the volatile addressable memory is dynamic random access memory or

static random access memory, and

wherein the ~~addressable~~ indirectly-read memory is chosen from the set of serial memory consisting of MultiMediaCard, SmartMedia Card, SD Card and Memory Stick.

42. (amended once) The wireless ~~communication~~ communications device system of claim 39, wherein the wireless ~~communication~~ communications device circuit comprises a power amplifier.

43. (amended once) The wireless ~~communication~~ communications device system of claim 39, wherein the wireless ~~communication~~ communications device circuit comprises a user interface.
